

WHAT IS CLAIMED:

1. A panel, comprising:
  - a first side, including:
    - at least one first connector configured to connect to electrical equipment and receive a plurality of signals from the electrical equipment, and
    - a plurality of second connectors respectively configured to connect to a plurality of network devices and deliver the plurality of signals to the plurality of network devices; and
    - a second side opposite from the first side, including:
      - a plurality of third connectors respectively coupled to at least one of the at least one first connector and the plurality of second connectors and configured to provide temporary connection to at least one of the electrical equipment and the plurality of network devices.
2. The panel of claim 1, wherein the second connectors include wire wrap pins.
3. The panel of claim 1, wherein the at least one first connector includes an integrated connector having a number of contact points therein.
4. The panel of claim 1, wherein the at least one first connector is electrically connected to the plurality of second connectors to deliver the plurality of signals from the

electrical equipment to the plurality of network devices.

5. The panel of claim 1, wherein the plurality of third connectors includes:  
a plurality of fourth connectors electrically connected to the at least one first connector  
and configured to provide temporary connections from the electrical equipment.
  
6. The panel of claim 5, wherein the plurality of third connectors further  
includes:  
a plurality of fifth connectors electrically connected to the plurality of second  
connectors and configured to provide temporary connections to the plurality of network devices  
from the plurality of fourth connectors.
  
7. The panel of claim 5, wherein the plurality of third connectors further  
includes:  
a plurality of sixth connectors electrically connected to the plurality of second  
connectors via a respective plurality of resistors and configured to provide high impedance  
connections to the plurality of network devices.
  
8. The panel of claim 1, wherein the at least one first connector and the  
plurality of second connectors each includes a primary group of connectors and a secondary  
group of connectors for connecting primary and secondary groups of signals between the  
electrical equipment and the plurality of network devices.

9. A timing output panel, comprising:

a rear portion, including:

✓ a plurality of network connectors respectively configured to connect to a plurality of network elements, and

✓ at least one timing connector connected to the plurality of network connectors and configured to connect to synchronization electronics; and

a front portion, including:

a plurality of equipment jacks corresponding to and electrically connected

to the plurality network connectors, the equipment jacks facilitating temporary connection of cables for testing or patching signals to the network elements, and

a plurality of timing jacks corresponding to and electrically connected to the at least one timing connector, the timing jacks facilitating temporary connection of cables for testing the synchronization electronics or patching to the equipment jacks.

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10. The timing output panel of claim 9, wherein the network connectors include wire wrap pins.

11. The timing output panel of claim 9, wherein the at least one timing connector includes a unitary connector having multiple pins corresponding to a number of signals from the synchronization electronics.

12. The timing output panel of claim 9, wherein the front portion further includes:

a plurality of monitor jacks electrically connected to the plurality of equipment jacks by a respective plurality of resistors.

13. The timing output panel of claim 9, wherein the network connectors include:

a primary group of network connectors, and  
a secondary group of network connectors that are spatially separated from the primary group of network connectors.

14. The timing output panel of claim 9, wherein the at least one timing connector include:

a primary timing connector, and  
a secondary timing connector that is spatially separated from the primary timing connector.

15. The timing output panel of claim 9, wherein the equipment jacks include:  
a primary group of equipment jacks, and  
a secondary group of equipment jacks that are spatially separated from the primary group of equipment jacks.

16. The timing output panel of claim 12, wherein the equipment jacks, the timing jacks, and the monitor jacks include an identical type of connector.

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17. A panel, comprising:
- a rear portion, including:
- means for connecting to a plurality of network elements, and
- means for connecting to synchronization electronics; and
- a front portion, including:
- means for testing at least one of the plurality of network elements and the synchronization electronics.
18. The panel of claim 17, wherein the front portion further includes:
- means for monitoring signals associated with the synchronization electronics.
19. The panel of claim 17, wherein the means for testing includes:
- means for removably accepting a patch cable.
20. The panel of claim 17, wherein the means for connecting to the plurality of network elements includes:
- means for perpetually receiving and holding one or more connection wires.
21. A system, comprising:
- a plurality of network elements, each network element respectively receiving an input timing signal;
- a timing signal generator configured to generate the synchronization signals for the plurality of network elements; and

a timing output panel connected between the plurality of network elements and the timing signal generator, the timing output panel including:

at least one first connector configured to receive the output signals from the timing signal generator,

a plurality of second connectors configured to send the output signals to the plurality of network elements, and

a plurality of third connectors spaced apart from the first and second connectors and configured to facilitate testing of the output signals and patching of the output signals.

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